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# **INFORMATION DISCLOSURE STATEMENT BY APPLICANT** ( Not for submission under 37 CFR 1.99)

Application Number		
Filing Date		
First Named Inventor	Gideon Kutz	
Art Unit		
Examiner Name	/Khai M. Nguyen/	
Attorney Docket Number	SC13079EI	

## **U.S. PATENTS**

Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	6166667		2000-12-26	Park	

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## **U.S. PATENT APPLICATION PUBLICATIONS**

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	1	20030018942	A1	2003-01-23	Seo	
	2	20020129317	A1	2002-09-12	Nicol et al	

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## **FOREIGN PATENT DOCUMENTS**

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	1	1204210	EP	A1	2002-05-08	Lucent Technologies		<input type="checkbox"/>
	2	1471677	EP	A1	2004-10-27	STMicroelectronics		<input type="checkbox"/>

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number		10596367 - GAU: 2819	
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	First Named Inventor	Gideon Kutz		
	Art Unit			
	Examiner Name	/Khai M. Nguyen/		
Attorney Docket Number		SC13079EI		

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	1	BICKERSTAFF et al; "A Unified turbo/Viterbi Channel Decoder for 3GPP Mobile Wireless in 0.18 $\mu$ m CMOS"; IEEE Journal of Solid-State Circuits, vol. 37, no. 11, November 2002 pp 1555-1564		<input type="checkbox"/>
	2	FAN-MIN LI et al; "Triple-Mode MAO/VA Timing Analysis for Unified Convolutional/Turbo Decoder Design"; IEEE Workshop Signal processing Systems, USA, October 2004, pp 280-285		<input type="checkbox"/>
	3	HUANG et al; "VLSI Design of Dual-Mode Viterbi/Turbo Decoder for 3GPP"; 2004 International Symposium Circuits and Systems, Canada, May 2004, vol. 2, pp 773-776		<input type="checkbox"/>
	4	CHIEN-CHING LIN et al; "A dual mode channel decoder for 3GPP2 mobile wireless communications"; 30th European Leuven, Solid State Circuits Conference, Belgium, 2004, pp 483-486 no month		<input type="checkbox"/>
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